IETF#80 – Prague
30 March 2011

RaptorQ Forward Error Correction Scheme for Object Delivery

draft-ietf-rmt-bb-fec-raptorq-05

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Status

- **Last updated:**
  - **2011-03-07**
- **State:**
  - **IESG Evaluation**
  - **On agenda of 2011-04-14 IESG telechat**

Changes from -04 to -05

- Minor rewordings
  - A few small rewordings to make it easier to read
  - A few typos fixed (e.g., s/generated generated/generated/)
  - Added a short description of math background needed to read
  - Expanded Section 5.1.1 and deleted Section 5.1.3 (relevant portions incorporated into 5.1.1)
    - Added informative reference and expanded description of notation in a couple of cases
- Added Section 4.4.3
  - Briefly describes receiver decoding with limited memory using sub-blocking
  - Informative – no changes to spec
- Changed "1" to "non-zero" as appropriate in Section 5.4.2.2
- Changes were based on feedback from Dave Harrington (AD) and Joel Halpern (Gen-art)

Additional changes to be made

- In Section 3.2.3
  - Change Z from a 12-bit field to a 8-bit field
    - Makes size of Z field (number of source blocks) consistent with Section 3.1
  - Change N from a 12-bit field to a 16-bit field
    - No impact – (only increases possible values of N)
    - Maintains sum of Z and N field sizes the same (3 octets)